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Re: U.S. Serial No: 10/707/863 Group Art Unit: 2826 Confirmation No: 2862 Docket No: BUR920030058US1 (16693)		Date:	5/6/2005
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Applicant(s): Ebenezer E. Eshun, et al.

Docket No.

BUR920030058US1 (16693)

Application No.
10/707,863Filing Date
January 19, 2004Examiner
Fazli ErdemGroup Art Unit
2826

Invention: **HIGH TOLERANCE TCR BALANCED HIGH CURRENT RESISTOR FOR RF CMOS AND RF SiGe BiCMOS APPLICATIONS AND CADENCED BASED HIERARCHICAL PARAMETERIZED CELL DESIGN KIT WITH TUNABLE TCR AND ESD RESISTOR BALLASTING FEATURE**

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AMENDMENT TRANSMITTAL LETTER (Large Entity)

Applicant(s): Ebenezer E. Eshun, et al.

Docket No.

BUR920030058US1 (16693)

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/707,863	January 19, 2004	Fazli Erdem	23389	2826	1862

Invention: **HIGH TOLERANCE TCR BALANCED HIGH CURRENT RESISTOR FOR RF CMOS AND RF SiGe BiCMOS APPLICATIONS AND CADENCED BASED HIERARCHICAL PARAMETERIZED CELL DESIGN KIT WITH TUNABLE TCR AND ESD RESISTOR BALLASTING FEATURE**

COMMISSIONER FOR PATENTS:

Transmitted herewith is an amendment in the above-identified application.

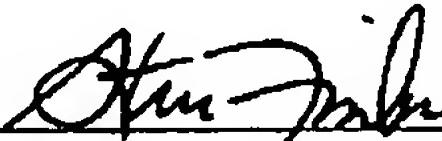
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INDEP. CLAIMS	3 -	6 =	0	x \$200.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00

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AMENDMENT TRANSMITTAL LETTER (Large Entity)				Docket No. BUR920030058US1 (16693)	
Applicant(s): Ebenezer E. Eshun, et al.					
Application No. 10/707,863	Filing Date January 19, 2004	Examiner Fazli Erdem	Customer No. 23389	Group Art Unit 2826	Confirmation No. 1862
Invention: HIGH TOLERANCE TCR BALANCED HIGH CURRENT RESISTOR FOR RF CMOS AND RF SiGe BiCMOS APPLICATIONS AND CADENCED BASED HIERARCHICAL PARAMETERIZED CELL DESIGN KIT WITH TUNABLE TCR AND ESD RESISTOR BALLASTING FEATURE					
COMMISSIONER FOR PATENTS:					
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CLAIMS AS AMENDED					
	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	14 -	30 =	0	x \$50.00	\$0.00
INDEP. CLAIMS	3 -	6 =	0	x \$200.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00
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 <i>Steven Fischman</i> <i>Signature</i>			Dated: May 6, 2005		
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**RECEIVED
CENTRAL FAX CENTER****MAY 06 2005****Applicant:** Ebenezer E. Eshun, et al.**Examiner:** Fazli Erdem**Serial No.:** 10/707,863**Art Unit:** 2826**Filed:** January 19, 2004**Docket:** BUR920030058US1 (16693)**For:** HIGH TOLERANCE TCR BALANCED
HIGH CURRENT RESISTOR FOR RF CMOS
AND RF SiGe BiCMOS APPLICATIONS AND
CADENCED BASED HIERARCHICAL
PARAMETERIZED CELL DESIGN KIT WITH
TUNABLE TCR AND ESD RESISTOR
BALLASTING FEATURE**Dated:** May 6, 2005**Confirmation No:** 1862Mail Stop: Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**RESPONSE**

Sir:

Responsive to the Office Action dated February 9, 2005 Applicants respectfully request the Examiner to reconsider the application in view of the following amendments and remarks.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.

CERTIFICATION OF FACSIMILE TRANSMISSION

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Dated: May 6, 2005



Steven Fischman